

IN THE CLAIMS:

Please amend the claims as follows:

1. (Currently Amended) A method for branching when a program is executed by a processor, where the program is stored in a program memory, and a variable memory and a table memory for storing fixed values are provided, the processor executing the following steps within a single jump instruction:

- a) addressing a first memory cell in the variable memory is ~~addressed~~,
- b) addressing a second memory cell in the variable memory is ~~addressed~~ on the basis of the content of the first memory cell, addressed in step a), and further parameters,
- c) addressing a third memory cell in the table memory ~~is addressed~~ on the basis of the content of the second memory cell, addressed in step b), and
- d) branching execution ~~branches~~ to a program address which is stored in ~~[[that]]~~ the third memory cell in the table memory ~~which was addressed in step c).~~

2. (Currently Amended) The method as claimed in claim 1, wherein ~~[[in]]~~ step b) ~~[[,]]~~ comprises addressing the second memory cell in the variable memory is ~~addressed~~ by the result of an instruction which processes the content of the first memory cell in the variable memory and further parameters.

3. (Currently Amended) The method as claimed in claim 1, ~~wherein~~ comprising addressing the variable memory ~~is addressed~~ using a first bit length a, and comprising addressing the table memory ~~is addressed~~ using a second bit length b, the first bit length a and the second bit length b being of different size.

4.-8. (Canceled)